

MAR 15 2010

S/N 10/608,870PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mark T. Bohr et al.

Examiner: Dao H. Nguyen

Serial No.: 10/608,870

Group Art Unit: 2818

Filed: 06-27-2003

Docket: P15335

Title: PMOS TRANSISTOR STRAIN OPTIMIZATION WITH RAISED JUNCTION REGIONS

REVOCATION AND POWER OF ATTORNEY  
CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent agents to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith:

**Customer Number: 59796**CERTIFICATE UNDER 37 CFR § 3.73(b)

Intel Corporation hereby certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor filed on June 27, 2003 and recorded on Reel 014517, Frame 0838. To the best of my knowledge and belief, title is in Intel Corporation, the assignee.

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Pam J. Matlock, am empowered to sign this certificate on behalf of Intel Corporation, the assignee.

I hereby declare that all statement made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true.

Please direct all correspondence in this case to:

**Rahul Engineer**  
**Customer Number: 59796**  
**Intel Corporation**  
**c/o CPA Global**  
**P.O. Box 52050**  
**Minneapolis, MN 55402**

Date 3/10/10

By Pam J. Matlock

Pam J. Matlock  
 Secretary of Patents  
 Intel Corporation  
 Phone: 408-765-1144